

Notice of References Cited	Application/Control No. 10/604,414	Applicant(s)/Patent Under Reexamination ANAND ET AL.	
	Examiner Mary Kate B. Baran	Art Unit 2857	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-6,662,133	12-2003	Engel et al.	702/117
	C	US-6,757,204	06-2004	Di Ronza et al.	365/200
	D	US-6,388,929	05-2002	Shimano et al.	365/201
	E	US-2002/0051400	05-2002	Yoneya et al.	365/225.7
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	G	US-2005/0018517	01-2005	Collura et al.	365/225.7
	H	US-6,567,323	05-2003	Pitts et al.	365/200
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Ouellette et al., "Shared fuse macro for multiple embedded memory with redundancy", May 2001, Custom Integrated Circuits, pages 191-194
	V	Cowan et al., "On-chip repair and an ATE independent fusing methodology", October 2002, Test Conference, pages 178-186
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.